G4/28/1/23

February 12, 2002

FB 2 6 2002

Commissioner of Patents and Trademarks Washington, D.C. 20231

Fr: George O. Saile, Reg. No. 19,572

20 McIntosh Drive

Poughkeepsie, N.Y. 12603

Subject:

Serial No. 10/040,233 11/07/01

Yu-Zung Chiou, Kuen-Hsien Lin, Ying-Lieh Chen, Shou-Yi Hsu

A NOVEL DESIGN AND FABRICATION
METHOD FOR FINGER N-TYPE DOPED
PHOTODIODES WITH HIGH SENSITIVITY
FOR CIS PRODUCTS

Grp. Art Unit:

INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation In An Application.

The following Patents and/or Publications are submitted to comply with the duty of disclosure under CFR 1.97-1.99 and 37 CFR 1.56. Copies of each document is included herewith.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on February 15, 2002.

Stephen B. Ackerman, Reg.# 37761

Signature/Date

1

U.S. Patent 6,027,955 to Lee et al., "Method of Making an Active Pixel Sensor Integrated with a Pinned Photodiode," discloses an active pixel sensor and n-well.

U.S. Patent 6,051,447 to Lee et al., "Partially Pinned Photodiode for Solid State Image Sensors," discloses a partially pinned photodiode with a well.

WILL U.S. Patent 4,904,607 to Riglet et al., "Method of Manufacturing an Integrated Infrared Detector," discloses a method of manufacturing an infrared detector having a buried PIN photodiode.

U.S. Patent 6,194,258 to Wuu, "Method of Forming an Image Sensor Cell and a CMOS Logic Circuit Device," discloses a method of forming an image sensor and a CMOS logic circuit device.

Sincerely,

Stephen B. Ackerman,

Reg. No. 37761

A 12/18/04

TSMC-01-102

WSV

U.S. Patent 6,027,955 to Lee et al., "Method of Making an Active Pixel Sensor Integrated with a Pinned Photodiode," discloses an active pixel sensor and n-well.

WSL

U.S. Patent 6,051,447 to Lee et al., "Partially Pinned Photodiode for Solid State Image Sensors," discloses a partially pinned photodiode with a well.

WSL

U.S. Patent 4,904,607 to Riglet et al., "Method of Manufacturing an Integrated Infrared Detector," discloses a method of manufacturing an infrared detector having a buried PIN photodiode.

4181

U.S. Patent 6,194,258 to Wuu, "Method of Forming an Image Sensor Cell and a CMOS Logic Circuit Device," discloses a method of forming an image sensor and a CMOS logic circuit device.

Sincerely,

Stephen B. Ackerman,

Reg. No. 37761

Chr.

1/4/05